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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/450,802	11/29/1999	JAY SETHURAM	STRAT-P013	8198
33031 75	590 01/22/2004		EXAMINER	
CAMPBELL STEPHENSON ASCOLESE, LLP 4807 SPICEWOOD SPRINGS RD.			DUONG, DUC T	
BLDG. 4, SUIT		*	ART UNIT	PAPER NUMBER
AUSTIN, TX	78759		2663	
			DATE MAILED: 01/22/2004	. [1

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)					
Advisory Action	09/450,802	SETHURAM, JAY					
	Examiner	Art Unit					
	Duc T. Duong	2663					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address							
THE REPLY FILED 01 December 2003 FAILS TO PLACE Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appear Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applica) a timely filed amendment whicl	ation. A proper repl n places the applica	y to a ation in				
PERIOD FOR RE	EPLY [check either a) or b)]						
a) The period for reply expiresmonths from the mailir							
b) A The period for reply expires on: (1) the mailing date of this a no event, however, will the statutory period for reply expire ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	later than SIX MONTHS from the mailing	g date of the final rejecti	ion.				
Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offitimely filed, may reduce any earned patent term adjustment. See 37 CFR 1.136(a).	of extension and the corresponding amo the shortened statutory period for reply ce later than three months after the mail	unt of the fee. The apportion or the final o	ropriate extension Office action; or				
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CF	·						
2. The proposed amendment(s) will not be entered because:							
(a) X they raise new issues that would require furth	er consideration and/or search (s	see NOTE below);					
(b) ☐ they raise the issue of new matter (see Note because of the second of the secon	pelow);						
 (c) they are not deemed to place the application issues for appeal; and/or 	n better form for appeal by mate	rially reducing or si	mplifying the				
(d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.							
NOTE: See Continuation Sheet.							
3. Applicant's reply has overcome the following rejection(s):							
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed	amendment				
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because:		dered but does NO	T place the				
6. The affidavit or exhibit will NOT be considered bed raised by the Examiner in the final rejection.	ause it is not directed SOLELY t	o issues which wer	e newly				
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we			and an				
The status of the claim(s) is (or will be) as follows:							
Claim(s) allowed:							
Claim(s) objected to:							
Claim(s) rejected: <u>1-11 and 14-20</u> .							
Claim(s) withdrawn from consideration:							

U.S. Patent and Trademark Office PTOL-303 (Rev. 11-03)

10. Other: ____

8. The drawing correction filed on ____ is a) approved or b) disapproved by the Examiner.

9. Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s). _____.





Continuation of 2. NOTE: The newly added limitation to the claims 1-3 has altered the scope and bound of the claim, therefore would require further search/consideration. Regarding to Applicant's argument on pages 6-10, Ishikawa fails to teach the buffer 103 generating a delay substantially equivalent to equal to the delay through said register 101 is directed to Fig. 2B. Herein, Ishikawa disclose a timing diagram, in which the buffer generating a delay Tb that's substantially equivalent or equal to the delay through said register Tq (noted Tq Td see col. 4 lines 14-33)

STEVEN H.D NGUYEN PRIMARY E (191NER